

Class	Subclass
ISSUE CLASSIFICATION	

FILED UNDER 35 U.S.C. 371

PATENT NUMBER

**U.S. UTILITY Patent Application**

**O.I.P.E.**

**PATENT DATE**

SCANNED

TRC, Q.A. AG

APPLICATION NO. 09/869912	CONT/PRIOR D F	CLASS 438 1171	SUBCLASS 13	ART UNIT 1765	EXAMINER KUNEMUND
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**TITLE : APPLICANTS**

Makoto Iida  
Masanori Kimura

Silicon single crystal wafer and production method thereof and soi  
wafer

**PTO-2040**  
**12/99**

ISSUING CLASSIFICATION

[illegible]

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	<b>Sheets Drwg.</b>	<b>Figs. Drwg.</b>	<b>Print Fig.</b>	<b>Total Claims</b>	<b>Print Claim for O.G.</b>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<div style="border: 1px solid black; height: 150px; margin-bottom: 5px;"></div> <div style="border: 1px solid black; height: 150px; margin-bottom: 5px;"></div> <div style="border: 1px solid black; height: 150px;"></div>			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____				<div style="border: 1px solid black; height: 100px; margin-bottom: 5px;"></div> <div style="border: 1px solid black; height: 100px;"></div>	
<input type="checkbox"/> The term of _____ months of this patent have been disclaimed.				<b>ISSUE DATE</b> Amount Due _____ Date Paid _____	
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